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(Signature & date)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of : November 12, 2003
Michael P. Belyansky et al.: Group Art Unit:
Serial No. 10/605,857 : Examiner:
Filed: 10/31/2003 : International Business Machines Corporation
2070 Route 52
Hopewell Junction, NY 12533

TITLE: HDP Process for High Aspect Ratio Gap Filling

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

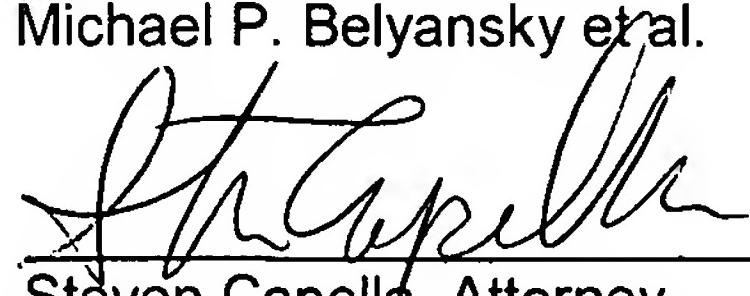
Sir:

Pursuant to the duty of disclosure set forth in 37 C.F.R. 1.56, and further pursuant to the provisions of 37 C.F.R. 1.97 and 1.98, applicants hereby respectfully submit copies of the prior patents and publications as listed on Form PTO-1449, attached hereto.

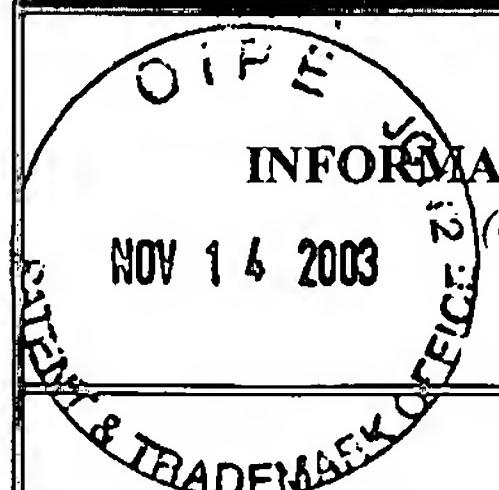
In citing these documents, no representation is made nor intended as to the pertinency or non-pertinency of the art, that better art than that listed is not available, or that other art is not applicable.

Respectfully submitted,
Michael P. Belyansky et al.

BY



Steven Capella, Attorney
Registration No. 33,086
Telephone No. 845-894-3669



INFORMATION DISCLOSURE CITATION

NOV 14 2003

(Use several sheets if necessary)

Docket Number (Optional)

FIS920030085US1

Application Number

10/605,857

Applicant(s)

Michael P. Belyansky et al.

Filing Date

October 31, 2003

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

OTHER DOCUMENTS (*Including Author, Title, Date, Pertinent Pages, Etc.*)

High-Density Plasma Chemical Vapor Deposition of Silicon-Based Dielectric Films for Integrated Circuits, S.V. Nguyen; IBM Journal of Research and Development, Volume 43, 1/2, 1999, Plasma Processing

EXAMINER _____ **DATE CONSIDERED** _____

EXAMINER: Initial if claim not considered. Include

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